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INSPEC - 1969 to date (INZZ)

### Accession number & update

6928207, B2001-06-3240C-018, C2001-06-4270-012; 20010521.

### Title

Design aspects of **superconducting-phase** quantum bits.

### Author(s)

Blatter-G; Geshkenbein-V-B; Ioffe-L-B.

### Author affiliation

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2001.

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EN.

### Publication type

J Journal Paper.

### Treatment codes

T Theoretical or Mathematical.

### Abstract

A **superconducting-phase** quantum bit (**qubit**) involves three or more Josephson junctions combined into a **superconducting loop** and defines one of the promising solid-state device implementations for quantum computing. Recently, so called pi junctions, Josephson junctions with a ground state characterized by a pi **-phase shift** across, have attracted much attention. We show how to make use of such pi junctions in the construction of **superconducting phase** qubits and discuss the advantage over conventional designs based on magnetically frustrated loops. Starting from a basic five-junction **lo p** with one pi junction, we show how to construct effective junctions with degenerate minima characterized by **phase** shifts 0 and pi and **superconducting-phase** switches. These elements are then combined into a **superconducting- phase qubit** which operates exclusively with switches, thus avoiding permanent contact with the environment through external biasing. The resulting **superconducting-phase** qubits can be understood as the macroscopic analog of the "quiet" s-wave-d-wave-s-wave Josephson-junction qubits introduced by Ioffe et al. Nature (London) 398, 679 (1999)ù. (36 refs).

*Date is good  
but published  
11 April 2001  
which is good!*

**Descriptors**

quantum-computing; superconducting-junction-devices.

**Keywords**

design aspects; **superconducting phase** quantum bits; **qubit**; Josephson junctions; **superconducting**  
**loop**; quantum computing; pi junctions; ground state; pi **phase shift**; **superconducting phase**  
switches; external biasing.

**Classification codes**

B3240C (**Superconducting** junction devices).

C4270 (Quantum computing theory).

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